

Notice of References Cited	Application/Control No. 10/695,953		Applicant(s)/Patent Under Reexamination CODE ET AL.	
	Examiner Charles E. Anya		Art Unit 2194	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2003/0037091 A1	02-2003	Nishimura et al.	709/103
*	B	US-6,625,750 B1	09-2003	Duso et al.	714/11
*	C	US-7,203,943 B2	04-2007	Shaffer, Larry J.	718/104
*	D	US-6,789,067 B1	09-2004	Liebenow, Frank	705/15
*	E	US-2002/0156669 A1	10-2002	Verhaegh et al.	705/8
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Derwent Information Ltd "Thread priority control apparatus in computer system - has priority modification unit which raises priority level of thread to predetermined limit" November 30, 1999.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.